Marathwada Mitramandal's
COLLEGE OF ENGINEERING
S.No.18, Plot No.5/3, Karvenagar, Pune-411 052
Accredited with 'A' Grade by NAAC
Accredited by NBA (Electrical and Mechanical Engg. Department)
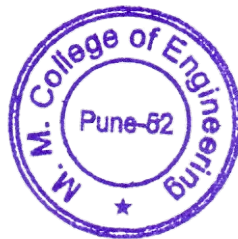*Recipient of 'Best College Award 2019' of SPPU*
Recognized under section 2(f) and 12B of UGC Act 1956

Criterion 2

2.5 : Evaluation Process and Reforms

2.5.1 Mechanism of internal/ external assessment is transparent and the grievance redressal system is time- bound and efficient

External Assessment
University Practical/ Oral Time Table

Marathwada MitraMandal's,
College of Engineering
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DEPARTMENT OF COMPUTER ENGINEERING

SPPU Practical / Oral Examination Time Table 20-21 Sem I (held in Apr21)

CLASS : BE (2015 Course)   BE 2020-21 Batch

Sr. No	Subject	Exam Head	Date - Day	Timing	Batch	Internal Examiner	External Examiner
1	Laboratory Practice I	Practical Term work	26-04-2021 Monday	9.00 am to 11.30 am	BE I Batch A	Ms. Pooja Dhule	Ms. Anita Shinde
				11.45 am to 2.15 pm	BE I Batch B		
				2.30 pm to 5.00 pm	BE I Batch C		
			27-04-2021 Tuesday	9.00 am to 11.30 am	BE II Batch A		
				11.45 am to 2.15 pm	BE II Batch B		
				2.30 pm to 5.00 pm	BE II Batch C and backlog		
2	Laboratory Practice II	Oral Term work	28-04-2021 Wednesday	9.00 am to 11.30 am	BE I Batch A	Ms. Jagruti Wagh	Ms. Sankirti Shiravale
				11.45 am to 2.15 pm	BE I Batch B		
				2.30 pm to 5.00 pm	BE I Batch C		
			29-04-2021 Thursday	9.00 am to 11.30 am	BE II Batch A		
				11.45 am to 2.15 pm	BE II Batch B		
				2.30 pm to 5.00 pm	BE II Batch C and backlog		
3	Project Stage I	Oral	30-04-2021 Friday	9.00 am to 04.00 pm	BE I & BE II	All Project Guides	
4	Project Stage II (Backlog)	Oral + TW	30-04-2021 Friday	10.00 am to 1.00 pm	Backlog Students (02 student)	Ms. Shubhada Mone	
5	Laboratory Practice III	Practical Term work	28-04-2021 Wednesday	10.00 am to 1.00 pm	Backlog students (02 students)	Ms. Ila savant	Ms. Anita Shinde
6	Laboratory Practice IV	Oral Term work	26-04-2021 Monday	10.00 am to 1.00 pm	Backlog students (02 students)	Ms. Pradnya Mehta	Ms. Sankirti Shiravale

PR-OR Coordinator

HOD, Comp Engg

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DEPARTMENT OF COMPUTER ENGINEERING

SPPU Practical / Oral Examination Time Table 20-21 Sem I (held in Apr21)

CLASS : TE (2015 Course)				TE 2020-21 Batch			
Sr. No	Subject	Exam Head	Date - Day	Timing	Batch	Internal Examiner	External Examiner
1	Skills Development Lab (SDL)	Oral	03-05-2021 Monday	9.00 am to 11.30 am	TE I Batch A	Dr. H. K. Khanuja	Ms. Asma Shaikh
				11.45 am to 2.15 pm	TE I Batch B		
				2.30 pm to 5.00 pm	TE I Batch C		
			04-05-2021 Tuesday	9.00 am to 11.30 am	TE II Batch A		
				11.45 am to 2.15 pm	TE II Batch B		
				2.30 pm to 5.00 pm	TE II Batch C		
2	Computer Networks Lab (CNL) Laboratory	Practical	05-05-2021 Wednesday	9.00 am to 11.30 pm	TE I Batch A	Ms. Geetha Chillarge	Ms. Sarita Sapkal
				11.45 am to 2.15 pm	TE I Batch B		
				2.30 pm to 5.00 pm	TE I Batch C		
			06-05-2021 Thursday	9.00 am to 11.30 am	TE II Batch A		
				11.45 am to 2.15 pm	TE II Batch B		
				2.30 pm to 5.00 pm	TE II Batch C		
3	Database Management System Lab (DBMSL)	Practical	07-05-2021 Friday	9.00 am to 11.30 am	TE I Batch A	Ms. Asma Shaikh	Ms. Pradnya Mehta
				11.45 am to 2.15 pm	TE I Batch B		
				2.30 pm to 5.00 pm	TE I Batch C		
			08-05-2021 Saturday	9.00 am to 11.30 am	TE II Batch A		
				11.45 am to 2.15 pm	TE II Batch B		
				2.30 pm to 5.00 pm	TE II Batch C		

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DEPARTMENT OF COMPUTER ENGINEERING

SPPU Practical / Oral Examination Time Table 20-21 Sem I (held in Apr- May21)

CLASS : SE (2015 Course)				Backlog			
Sr. No	Subject	Exam Head	Date - Day	Timing	Batch	Internal Examiner	External Examiner
1	OOPL	Practical	10-05-2021 Monday	10.00 am to 1.00 pm	Backlog (01 student)	Dr. Swati Shekapure	Ms. Anita Shinde
2	CGL	Practical	11-05-2021 Tuesday	10.00 am to 1.00 pm	Backlog (01 student)	Ms. Vandana Rupnar	Ms. Pooja Dhule
3	MPL	Practical	12-05-2021 Wednesday	10.00 am to 1.00 pm	Backlog (05 students)	Ms. Rupali Dalvi	Ms. Jagruti Wagh
4	ADSL	Practical	13-05-2021 Thursday	10.00 am to 1.00 pm	Backlog (02 students)	Ms. Anita Shinde	Ms.Shubhada Mone

CLASS : SE (2019 Course)				SE 2020-21 Batch			
Sr. No	Subject	Exam Head	Date - Day	Timing	Batch	Internal Examiner	External Examiner
1	Data Structures Laboratory	Practical	10-05-2021 Monday	9.00 am to 12.00 pm	SE I Batch A	Ms.Shubhada Mone	Dr. Aarti Agarkar
				12.30 pm to 3.30 pm	SE I Batch B		
			11-05-2021 Tuesday	9.00 am to 12.00 pm	SE I Batch C		
				12.30 pm to 3.30 pm	SE II Batch A		
			12-05-2021 Wednesday	9.00 am to 12.00 pm	SE II Batch B		
				12.30 pm to 3.30 pm	SE II Batch C		
2	OOP and Computer Graphics Laboratory	Practical	12-05-2021 Wednesday	9.00 am to 12.00 pm	SE I Batch A	Dr. Swati Shekapure	Ms. Vandana Rupnar
				12.30 pm to 3.30 pm	SE I Batch B		
			13-05-2021 Thursday	9.00 am to 12.00 pm	SE I Batch C		
				12.30 pm to 3.30 pm	SE II Batch A		
			14-05-2021 Friday	9.00 am to 12.00 pm	SE II Batch B		
				12.30 pm to 3.30 pm	SE II Batch C		

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Marathwada MitraMandal's, Marathwada MitraMandal's, College Of Engineering, Pune
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DEPARTMENT OF COMPUTER ENGINEERING

SPPU Practical / Oral Examination Time Table 20-21 Sem II (held in June 2021)

CLASS : BE (2015 Course) **BE 2020-21 Batch**

Sr. No	Subject	Exam Head	Date - Day	Timing	Batch	Internal Examiner	External Examiner
1	Laboratory Practice III	Practical Term work	09/06/2021 Wednesday	9.00 am to 11.30 am	BE I Batch A	Ms. Ila Savant	Ms. Anita Shinde
				11.45 am to 2.15 pm	BE I Batch B		
				2.30 pm to 5.00 pm	BE I Batch C		
			10/06/2021 Thursday	9.00 am to 11.30 am	BE II Batch A		
				11.45 am to 2.15 pm	BE II Batch B		
				2.30 pm to 5.00 pm	BE II Batch C and backlog		
2	Laboratory Practice IV	Oral Term work	11/06/2021 Friday	9.00 am to 11.30 am	BE I Batch A	Ms. Pradnya Mehta	Ms. Sankirti Shiravale
				11.45 am to 2.15 pm	BE I Batch B		
				2.30 pm to 5.00 pm	BE I Batch C		
			12/06/2021 Saturday	9.00 am to 11.30 am	BE II Batch A		
				11.45 am to 2.15 pm	BE II Batch B		
				2.30 pm to 5.00 pm	BE II Batch C and backlog		
3	Project Stage II	Oral + TW	14/06/2021 Monday	10.00 am to 1.00 pm	All Students	Ms.Shubhada Mone	Dr. Aarti Agarkar

PR-OR Coordinator   **HOD, Comp Engg**

Marathwada MitraMandal's
College of Engineering

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DEPARTMENT OF COMPUTER ENGINEERING

SPPU Practical / Oral Examination Time Table 20-21 Sem II (held in June 2021)

Sr. No	Subject	Exam Head	Date - Day	Timing	Batch	Internal Examiner	External Examiner
CLASS : TE (2015 Course)				**TE 2020-21 Batch**			
1	SP & OS Lab	Practical	10/06/2021 Thursday	9.00 am to 11.30 am	TE I Batch A	Ms. Pooja Dhule	Ms. Mayuri Rathod
				11.45 am to 2.15 pm	TE I Batch B		
				2.30 pm to 5.00 pm	TE I Batch C		
			11/06/2021 Friday	9.00 am to 11.30 am	TE II Batch A		
				11.45 am to 2.15 pm	TE II Batch B		
				2.30 pm to 5.00 pm	TE II Batch C		
2	Web Technology Lab	Practical	12/06/2021 Saturday	9.00 am to 11.30 pm	TE I Batch A	Ms. Asma Shaikh	Dr. Swati Shekapure
				11.45 am to 2.15 pm	TE I Batch B		
				2.30 pm to 5.00 pm	TE I Batch C		
			14/06/2021 Monday	9.00 am to 11.30 am	TE II Batch A		
				11.45 am to 2.15 pm	TE II Batch B		
				2.30 pm to 5.00 pm	TE II Batch C		

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DEPARTMENT OF COMPUTER ENGINEERING

SPPU Practical / Oral Examination Time Table 20-21 Sem II (held in June 2021)

CLASS	SE (2019 Course)			SE 2020-21 Batch			
Sr. No	Subject	Exam Head	Date - Day	Timing	Batch	Internal Examiner	External Examiner
1	Data Structure & Algorithm Lab	Practical	10/06/2021 Thursday	9.00 am to 11.30 am	SE I Batch A	Dr. Aarti Agarkar	Ms.Vandana Rupnar/ Ms. Pornima Muchate
				11.45 am to 2.15 pm	SE I Batch B		
				2.30 pm to 5.00 pm	SE I Batch C		
			11/06/2021 Friday	9.00 am to 11.30 am	SE II Batch A		
				11.45 am to 2.15 pm	SE II Batch B		
				2.30 pm to 5.00 pm	SE II Batch C		
2	Microprocessor Lab	Oral	12/06/2021 Saturday	9.00 am to 11.30 am	SE I Batch A	Ms. Jagruti Wagh	Ms. Rupali Dalvi
				11.45 am to 2.15 pm	SE I Batch B		
				2.30 pm to 5.00 pm	SE I Batch C		
			14/06/2021 Monday	9.00 am to 11.30 am	SE II Batch A		
				11.45 am to 2.15 pm	SE II Batch B		
				2.30 pm to 5.00 pm	SE II Batch C		

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HOD, Comp Engg

Marathwada MitraMandal's, College Of Engineering, Pune

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DEPARTMENT OF COMPUTER ENGINEERING

SPPU Practical / Oral Examination Time Table 20-21 Sem I (held in June-July 21)

| Sr. No | CLASS : BE (2015 Course) | | | BE 2020-21 Batch | | | |
	Subject	Exam Head	Date - Day	Timing	Batch	Internal Examiner	External Examiner
1	Laboratory Practice I	Practical Term work	30-06-2021 Wednesday	9.15 am to 10.00 am	MAGAR SONY	Ms. Pooja Dhule	Ms. Anita Shinde
2	Laboratory Practice II	Oral Term work	01-07-2021 Thursday	9.15 am to 10.00 am	MAGAR SONY	Ms. Jagruti Wagh	Ms. Sankirti Shiravale
3	Project Stage I	Oral	02-07-2021 Friday	9.15 am to 10.00 am	MAGAR SONY	Ms. Shubhada Mone	

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DEPARTMENT OF COMPUTER ENGINEERING

	SPPU Practical / Oral Examination Time Table 20-21 Sem I (held in July 21)						
CLASS : TE (2015 Course)				**TE 2020-21 Batch**		**Internal Examiner**	**External Examiner**
Sr. No	Subject	Exam Head	Date - Day	Timing	Students Name		
1	Skills Development Lab (SDL)	Oral	01-07-2021 Thursday	2.00 pm to 3.00pm	KAMBLE SNEHAL SUNIL SHINDE BHAVANA SHIVANATHRAO	Ms. Asma Shaikh	Dr. H. K. Khanuja
			15-07-2021	12:00 pm to 1:00 pm	Sanket Patole Muhammad Warunkar		
2	Database Management System Lab (DBMSL)	Practical	02-07-2021 Friday	9.15 am to 10.00 am	KAMBLE SNEHAL SUNIL SHINDE BHAVANA SHIVANATHRAO	Ms. Asma Shaikh	Ms. Pradnya Mehta
			17-07-2021	9.15 am to 10.00 am	Sanket Patole Muhammad Warunkar		
3	Computer Networks Lab (CNL) Laboratory	Practical	03-07-2021 Saturday	10.30 am to 11.15 am	KAMBLE SNEHAL SUNIL SHINDE BHAVANA SHIVANATHRAO	Ms. Geetha Chillarge	Ms. Sarita Sapkal
			16/07/2021	9:30 to 10:30 am	Sanket Patole Muhammad Warunkar		

PR-OR Coordinator

HOD, Comp Engg

Date: 01/06/2021

TIME TABLE OF SPPU ORAL, PRACTICAL EXAMINATION - JUNE - 2021

Semester - II, A.Y. - 2020-21

Sr. No.	Year & Course	Subject	Exam Head	Dates	Exam seat No	Timings
1	SE - 2019 PAT	Electrical Machine I	PR	10-Jun-21	S190452501 to S1904525528	3.00 PM to 5.00 PM
				11-Jun-21	S1904525529 to S190452556	3.00 PM to 5.00 PM
				12-Jun-21	S190452557 to S190452585	3.00 PM to 5.00 PM
2	SE - 2019 PAT	Numerical Methods and Computer Programming	PR	10-Jun-21	S1904525529 to S190452556	3.00 PM to 5.00 PM
				11-Jun-21	S190452557 to S190452585	3.00 PM to 5.00 PM
				12-Jun-21	S190452501 to S1904525528	3.00 PM to 5.00 PM
3	SE - 2019 PAT	Fundamentals of Microprocesor and Applications	OR	10-Jun-21	S190452557 to S190452585	3.00 PM to 5.00 PM
				11-Jun-21	S190452501 to S1904525528	3.00 PM to 5.00 PM
				12-Jun-21	S1904525529 to S190452556	3.00 PM to 5.00 PM

Note: 1. Meeting link should share with students by respective subject teacher.

2. Students will appear for oral on Google meet.

3. This session will be recorded

OR / PR Coordinator

HOD Electrical

Marathwada Mitra Mandal's College of Engineering, Karvenagar, Pune - 52
Accredited with 'A' Grade by NAAC, Recipient of " Best College Award 2019" by SPPU
NBA Accredited (Mechanical and Electrical Departments)
Department of Electrical Engineering

Date : 01/06/2021

TIME TABLE OF SPPU ORAL, PRACTICAL EXAMINATION - JUNE - 2021
Semester - II, A.Y. - 2020-21

Sr. No.	Year & Course	Subject	Exam Head	Dates	Exam seat No	Timings
1	TE -2015	Power System II	PR	10-Jun-21	T150452501 to T150452507 T150452509 to T150452512 T150452514 to T150452528	12.45 PM to 02.45 PM
				11-Jun-21	T150452529 to T150452543 T150452545 to T150452549 T150452551 to T150452554	12.45 PM to 02.45 PM
				12-Jun-21	T150452555 to T150452560 T150452563 to T150452581	12.45 PM to 02.45 PM
2	TE -2015	Control System I	OR	10-Jun-21	T150452529 to T150452543 T150452545 to T150452549 T150452551 to T150452554	12.45 PM to 02.45 PM
				11-Jun-21	T150452555 to T150452560 T150452563 to T150452581	12.45 PM to 02.45 PM
				12-Jun-21	T150452501 to T150452507 T150452509 to T150452512 T150452514 to T150452528	12.45 PM to 02.45 PM
3	TE-2015	Design of Electrical Machine	OR	10-Jun-21	T150452555 to T150452560 T150452563 to T150452581	12.45 PM to 02.45 PM
				11-Jun-21	T150452501 to T150452507 T150452509 to T150452512 T150452514 to T150452528	12.45 PM to 02.45 PM
				12-Jun-21	T150452529 to T150452543 T150452545 to T150452549 T150452551 to T150452554	12.45 PM to 02.45 PM

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OR / PR Coordinator

HOD Electrical

Marathwada Mitra Mandal's College of Engineering, Karvenagar, Pune - 52
Accredited with 'A' Grade by NAAC, Recipient of " Best College Award 2019" by SPPU
NBA Accredited (Mechanical and Electrical Departments)

Department of Electrical Engineering

Date: 01/06/2021

TIME TABLE OF SPPU ORAL , PRACTICAL EXAMINATION - JUNE - 2021

Semester - II, A.Y. - 2020-21

Sr. No.	Year & Course	Subject	Exam Head	Dates	Exam seat No	Timings
1	BE-2015	Switchgear and Protection	OR	10-Jun-21	B150452501 to B150452527	10.00 AM to 12.00 Noon
				11-Jun-21	B150452528 to B150452555	10.00 AM to 12.00 Noon
				12-Jun-21	B150452556 to B150452581	10.00 AM to 12.00 Noon
2	BE-2015	Power Electronics Contolled Drives	PR	10-Jun-21	B150452528 to B150452555	10.00 AM to 12.00 Noon
				11-Jun-21	B150452556 to B150452581	10.00 AM to 12.00 Noon
				12-Jun-21	B150452501 to B150452527	10.00 AM to 12.00 Noon
3	BE-2015	High Voltahe Engineering	OR	10-Jun-21	B150452556 to B150452581	10.00 AM to 12.00 Noon
				11-Jun-21	B150452501 to B150452527	10.00 AM to 12.00 Noon
				12-Jun-21	B150452528 to B150452555	10.00 AM to 12.00 Noon
4	BE-2015	Project II	OR	14-Jun-21	B150452501 to B150452581	Separate Timetable for Project II

Note: 1. Meeting link should share with students by respective subject teacher.
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OR / PR Coordinator

HOD Electrical

DEPARTMENT OF ELECTRICAL ENGINEERING

In-SEM Examination Time Table
(A.Y. 2020-21, SEM-II June 2021 Exam)

S.E. (Electrical Engineering)
(2019 Pattern)

S.N.	SUBJECT	DAY & DATE	TIME
1	Power System –I (PS-I)	Saturday, 05/06/2021	11 :00 AM to 11.40 AM
2	Electrical Machines-I (EM-I)	Sunday, 06/06/2021	11 :00 AM to 11.40 AM
3	Network Analysis (NA)	Monday, 07/06/2021	11 :00 AM to 11.40 AM
4	Numerical Methods & Computer Programming (NMCP)	Tuesday, 08/06/2021	11 :00 AM to 11.40 AM
5	Fundamental of Microcontroller and Applications (FMA)	Wednesday, 09/06/2021	11 :00 AM to 11.40 AM

Important Note:

1. In-Sem Examination will be conducted on Moodle platform

2. For FE & SE, In-Sem Examination will be conducted on Unit 1 and Unit 2.

3. In-Sem Examination for all classes will be of **30 Marks and 40 minutes duration**. There will be 40 questions (MCQ type for 1 mark each) out of which 30 correctly answered questions will be considered for evaluation.

Departmental Exam Coordinators

HOD

Marathwada Mitra Mandal's

COLLEGE OF ENGINEERING, PUNE-52

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DEPARTMENT OF ELECTRICAL ENGINEERING

In-SEM Examination Time Table
(A.Y. 2020-21, SEM-II June 2021 Exam)

T.E. (Electrical Engineering)
(2015 Pattern)

S.N.	SUBJECT	DAY & DATE	TIME
1	Power System –II (PS-II)	Saturday, 05/06/2021	02:00 PM – 02.40 PM
2	Control System –I (CS-I)	Sunday, 06/06/2021	02:00 PM – 02.40 PM
3	Utilization of Electrical Energy (UEE)	Monday, 07/06/2021	02:00 PM – 02.40 PM
4	Design of Electrical Machines (DEM)	Tuesday, 08/06/2021	02:00 PM – 02.40 PM
5	Energy Audit and Management (EAM)	Wednesday, 09/06/2021	02:00 PM – 02.40 PM

Important Note:

1. In-Sem Examination will be conducted on Moodle platform

2. For TE & BE, In-Sem Examination will be conducted on Unit 1, Unit 2 & Unit 3

3. In-Sem Examination for all classes will be of **30 Marks and 40 minutes duration**. There will be 40 questions (MCQ type for 1 mark each) out of which 30 correctly answered questions will be considered for evaluation.

Departmental Exam Coordinators   HOD

Marathwada Mitra Mandal's

COLLEGE OF ENGINEERING, PUNE-52

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DEPARTMENT OF ELECTRICAL ENGINEERING

In-SEM Examination Time Table
(A.Y. 2020-21, SEM-II June 2021 Exam)

B.E. (Electrical Engineering)
(2015 Pattern)

S.N.	SUBJECT	DAY & DATE	TIME
1	Switchgear and Protection (SGP)	Saturday, 05/06/2021	05: 00 PM – 05.40 PM
2	Power Electronic Controlled Drives (PECD)	Sunday, 06/06/2021	05: 00 PM – 05.40 PM
3	High Voltage Engineering (HVE)	Monday, 07/06/2021	05: 00 PM – 05.40 PM
4	Smart Grid (SG)	Tuesday, 08/06/2021	05: 00 PM – 05.40 PM

Important Note:

1. In-Sem Examination will be conducted on Moodle platform

2. For TE & BE, In-Sem Examination will be conducted on Unit 1, Unit 2 & Unit 3

3. In-Sem Examination for all classes will be of **30 Marks and 40 minutes duration**. There will be 40 questions (MCQ type for 1 mark each) out of which 30 correctly answered questions will be considered for evaluation.

Departmental Exam Coordinators

HOD

Marathwada Mitra Mandals
COLLEGE OF ENGINEERING PUNE

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NBA Accredited

Department of Electrical Engineering

Time Table For Online Oral/ Practical Examination

Examination – Oct/Nov 2020 (Held in April 2021)

SE Electrical   A. Y. 2020-21   SEM-I

Sr. N0	Day and Date	Material Science	Analog and Digital Electronics	Electrical Measurements and Instrumentation
1	Wednesday, April 07/04/2021	S190452501 to S190452528 9.15 AM to 11.15 AM	S190452529 to S190452556 9.15 AM to 11.15 AM	S190452557 to S190452585 9.15 AM to 11.15 AM
2	Thursday, April 08/04/2021	S190452557 to S190452585 11.30 AM to 1.30 PM	S190452501 to S190452528 11.30 AM to 1.30 PM	S190452529 to S190452556 11.30 AM to 1.30 PM
3	Friday, April 09/04/2021	S190452529 to S190452556 2 .00 PM to 4.00 PM	S190452557 to S190452585 2 .00 PM to 4.00 PM	S190452501 to S190452528 2 .00 PM to 4.00 PM

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Co-ordinator

HOD

Department of Electrical Engineering

Time Table For Online Oral/ Practical Examination

Examination – Oct/Nov 2020 (Held in April 2021)

TE Electrical   A. Y. 2020-21   SEM-I

Sr. N0	Day and Date	Advance Microcontroller and Application	Electrical Machine II	Power Electronics
1	Wednesday , April 07/04/2021	T150452501 to T150452525 11.30 AM to 1.30 PM	T150452526 to T150452550 11.30 AM to 1.30 PM	T150452551 to T150452575 11.30 AM to 1.30 PM
2	Thursday , April 08/04/2021	T150452551 to T150452575 2 .00 PM to 4.00 PM	T150452501 to T150452525 2 .00 PM to 4.00 PM	T150452526 to T150452550 2 .00 PM to 4.00 PM
3	Friday , April 09/04/2021	T150452526 to T150452550 9.15 AM to 11.15 AM	T150452551 to T150452575 9.15 AM to 11.15 AM	T150452501 to T150452525 9.15 AM to 11.15 AM

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Co-ordinator

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Department of Electrical Engineering

Time Table For Online Oral/ Practical Examination

Examination – Oct/Nov 2020 (Held in April 2021)

BE Electrical   A. Y. 2020-21   SEM-I

Sr. N0	Day and Date	PSOC	PLC and SCADA	CS II
1	Wednesday , April 07/04/2021	B150452501 to B150452527 9.15 AM to 11.15 AM	B150452528 to B150452555 9.15 AM to 11.15 AM	B150452556 to B150452581 9.15 AM to 11.15 AM
2	Thursday, April 08/04/2021	B150452528 to B150452555 11.30 AM to 1.30 PM.	B150452556 to B150452581 11.30 AM to 1.30 PM.	B150452501 to B150452527 11.30AM to 1.30 PM.
3	Friday, April 09/04/2021	B150452556 to B150452581 2 .00 PM to 4.00 PM	B150452501 to B150452527 2.00 PM to 4.00 PM	B150452528 to B150452555 2 .00 PM to 4.00 PM

Note: 1. Meeting link should share with students by respective subject teacher.
2. Students will appear for oral on Google meet.
3. This session will be recorded

Co-ordinator

HOD

Marathwada Mitra Mandal's College of Engineering, Karvenagar, Pune - 52
Accredited with 'A' Grade by NAAC, Recipient of "Best College Award 2019" by SPPU
Department of Electronics & Telecommunication Engineering

Date: 07/06/2021

TIME TABLE OF SPPU PRACTICAL, ORAL & TERMWORK EXAMINATION
Semester - II, A.Y. - 2020-21

Sr. No.	Year & Course	Subject	Exam Head	Dates	Roll No.	Timings
1	BE-2015	Lab Practice –IV (Elective III)	OR	11-Jun-21	B150453001 to B150453025	10.00 am to 12.00 pm
					B150453026 to B150453050	12.30 pm to 2.30 pm
					B150453051 to B150453075	3.00 pm to 5.00 pm
				12-Jun-21	B150453076 to B150453100	10.00 am to 12.00 pm
					B150453101 to B150453125	12.30 pm to 2.30 pm
					B150453126 to B150453146 & remaining if any	3.00 pm to 5.00 pm
2	BE-2015	Lab Practice –III (MC+BCS)	TW+PR	14-Jun-21	B150453001 to B150453025	10.00 am to 12.00 pm
					B150453026 to B150453050	12.30 pm to 2.30 pm
					B150453051 to B150453075	3.00 pm to 5.00 pm
				15-Jun-21	B150453076 to B150453100	10.00 am to 12.00 pm
					B150453101 to B150453125	12.30 pm to 2.30 pm
					B150453126 to B150453146 & remaining if any	3.00 pm to 5.00 pm
3	BE-2015	Project Stage II	PR+OR	11th to 15th June 2021	**All Students**	4.00 pm to 6.00 pm

Note :- For Project Stage II Exams, Project Groups are hereby informed to contact their Project Guides.

OR / PR Coordinator

HEAD
Dept. of E&TC Engg.

Marathwada Mitra Mandal's College of Engineering, Karvenagar, Pune - 52

Accredited with 'A' Grade by NAAC, Recipient of "Best College Award 2019" by SPPU

Department of Electronics & Telecommunication Engineering

Date: 07/06/2021

TIME TABLE OF SPPU PRACTICAL, ORAL & TERMWORK EXAMINATION

Semester - II, A.Y. - 2020-21

Sr. No.	Year & Course	Subject	Exam Head	Dates	Roll No.	Timings	IE+ EE	NT
1	SE-2019	Principle of Communication Systems Lab	PR	10-Jun-21	S190453001 to S190453026	10.00 am to 12.00 pm	*MAD AMS	AHS
					S190453027 to S190453052	12.30 pm to 2.30 pm		
					S190453053 to S190453078	3.00 pm to 5.00 pm		
				11-Jun-21	S190453079 to S190453104	10.00 am to 12.00 pm		
					S190453105 to S190453130	12.30 pm to 2.30 pm		
					S190453131 to S190453153 and remaining if any.	3.00 pm to 5.00 pm		
2	SE-2019	Object Oriented Programming Lab	OR	12-Jun-21	S190453001 to S190453026	10.00 am to 12.00 pm	*SAJ MER	AHS
					S190453027 to S190453052	12.30 pm to 2.30 pm		
					S190453053 to S190453078	3.00 pm to 5.00 pm		
				14-Jun-21	S190453079 to S190453104	10.00 am to 12.00 pm	*SAJ #	AHS
					S190453105 to S190453130	12.30 pm to 2.30 pm		
					S190453131 to S190453153 and remaining if any.	3.00 pm to 5.00 pm		
3	SE-2019	Data Analytics Lab	OR	14-Jun-21	S190453001 to S190453026	10.00 am to 12.00 pm	PSS HNB	JBD
					S190453027 to S190453052	12.30 pm to 2.30 pm		
					S190453053 to S190453078	3.00 pm to 5.00 pm		
				15-Jun-21	S190453079 to S190453104	10.00 am to 12.00 pm		
					S190453105 to S190453130	12.30 pm to 2.30 pm		
					S190453131 to S190453153 and remaining if any.	3.00 pm to 5.00 pm		

\# - Free faculty will be External Examiner   * - Internal Examiner
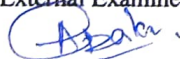
OR / PR Coordinator

HEAD
Dept. of E&TC Engg.

Marathwada Mitra Mandal's College of Engineering, Karvenagar, Pune - 52
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Department of Electronics & Telecommunication Engineering

Date: 07/06/2021

TIME TABLE OF SPPU PRACTICAL, ORAL & TERMWORK EXAMINATION
Semester - II, A.Y. - 2020-21
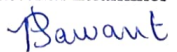
Sr. No.	Year & Course	Subject	Exam Head	Dates	Roll No.	Timings	IE+ EE	NT
1	TE-2015	Power and ITCT Lab	PR	11-Jun-21	T150453001 to T150453027	10.00 am to 12.00 pm	*JMB SST	JBD
					T150453028 to T150453054	12.30 pm to 2.30 pm		
					T150453055 to T150453086	3.00 pm to 5.00 pm		
				12-Jun-21	T150453087 to T150453112	10.00 am to 12.00 pm	*JMB SST	RPK
					T150453113 to T150453139	12.30 pm to 2.30 pm		
					T150453140 to T150453164 & remaining if any.	3.00 pm to 5.00 pm		
2	TE-2015	Advanced Processors and System Programming Lab	PR	14-Jun-21	T150453001 to T150453027	10.00 am to 12.00 pm	*AGD MER	RPK
					T150453028 to T150453054	12.30 pm to 2.30 pm		
					T150453055 to T150453086	3.00 pm to 5.00 pm		
				15-Jun-21	T150453087 to T150453112	10.00 am to 12.00 pm	*AGD MER	RPK
					T150453113 to T150453139	12.30 pm to 2.30 pm		
					T150453140 to T150453164 & remaining if any.	3.00 pm to 5.00 pm		
3	TE-2015	Employability Skills and Mini Project	OR	11-Jun-21	T150453087 to T150453112	10.00 am to 12.00 pm	*RRM VBD	VGP
					T150453113 to T150453139	12.30 pm to 2.30 pm		
					T150453140 to T150453164 & remaining if any.	3.00 pm to 5.00 pm		
				12-Jun-21	T150453001 to T150453027	10.00 am to 12.00 pm	*RRM VBD	VGP
					T150453028 to T150453054	12.30 pm to 2.30 pm		
					T150453055 to T150453086	3.00 pm to 5.00 pm		

\# - Free faculty will be External Examiner   * - Internal Examiner

OR / PR Coordinator

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Dept. of E&TC Engg.

Marathwada Mitramandal's
COLLEGE OF ENGINEERING
S.No.18, Plot No.5/3, Karvenagar, Pune-411 052
Accredited with 'A' Grade by NAAC
Accredited by NBA (Electrical and Mechanical Engg. Department)
Recipient of 'Best College Award 2019' of SPPU
Recognized under section 2(f) and 12B of UGC Act 1956

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External Assessment
University Theory Examination Time Table

SAVITRIBAI PHULE PUNE UNIVERSITY

(Formerly University of Pune)

Timetable For Fresh & Backlog Online Examination of October/November 2021

Faculty of Science & Technology : B)Engineering

Course_Name	SUBJECT CODE	SUBJECT NAME	YEAR	SEM.	Exam_Date	TIME SLOT
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404181	VLSI DESIGN & TECHNOLOGY	4	7	17/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404182	COMPUTER NETWORKS & SECURITY	4	7	18/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404183	RADIATION & MICROWAVE TECHNIQUES	4	7	21/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404184A	DIGITAL IMAGE AND VIDEO PROCESSING	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404184C	EMBEDDED SYSTEMS & RTOS	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404184D	INTERNET OF THINGS	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404185B	ELECTRONICS PRODUCT DESIGN	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404185D	ARTIFICIAL INTELLIGENCE	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404185E	ELECTRONICS IN AGRICULTURE	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	410301	Machine Learning (HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	410401	Internet of Things and Embeded Security(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	410501	Machine Learning and Data Science(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	410701	Virtual Reality in Game Development(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	411601	Machine Learning for Internet of Things(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404189	MOBILE COMMUNICATION	4	8	25/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404190	Broadband Communication Systems	4	8	26/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404191A	MACHINE LEARNING	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404191B	PLC S AND AUTOMATION	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404191D	SOFTWARE DEFINED RADIO	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404191E	AUDIO VIDEO ENGINEERING	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404191C	AUDIO AND SPEECH PROCESSING	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404192C	WIRELESS SENSOR NETWORKS	4	8	02/Mar/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(ELECTRONICS & TELECOMMU.)	404192D	RENEWABLE ENERGY SYSTEMS	4	8	02/Mar/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT)(INSTRUMENTATION & CONTROL	406261	PROCESS DYNAMICS AND CONTROL	4	7	17/Feb/2022	04.00 PM To 06.00 PM

Course_Name	SUBJECT CODE	SUBJECT NAME	YEAR	SEM.	Exam_Date	TIME SLOT
B.E.(2015 PAT.)(CIVIL)	401009C	HYDROPOWER ENGINEERING	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(CIVIL)	401009D	AIR POLLUTION AND CONTROL	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(CIVIL)	401009F	AIRPORT & BRIDGE ENGG.	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(CIVIL)	401010A	CONSTRUCTION MANAGEMENT	4	8	02/Mar/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410241	HIGH PERFORMANCE COMPUTING	4	7	17/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410242	ARTIFICIAL INTELLIGENCE AND ROBOTICS	4	7	18/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410243	DATA ANALYTICS	4	7	21/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410244A	DIGITAL SIGNAL PROCESSING	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410244B	SOFTWARE ARCHITECTURE AND DESIGN	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410244C	PERVASIVE AND UBIQUITOUS COMPUTING	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410244D	DATA MINING AND WAREHOUSING	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410245A	DISTRIBUTED SYSTEMS	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410245B	SOFTWARE TESTING AND QUALITY ASSURANCE	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410245C	OPERATIONS RESEARCH	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410245D	MOBILE COMMUNICATION	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410301	Machine Learning (HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410401	Internet of Things and Embeded Security(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410501	Machine Learning and Data Science(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410701	Virtual Reality in Game Development(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	411601	Machine Learning for Internet of Things(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410250	MACHINE LEARNING	4	8	25/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410251	INFORMATION AND CYBER SECURITY	4	8	26/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410252C	EMBEDDED & REAL TIME O.S.	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410252D	SOFT COMPU. & OPTIM. ALGO.	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410252B	COMPILERS	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410253B	HUMAN COMPUTER INTERFACE	4	8	02/Mar/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(COMPUTER)	410253C	CLOUD COMPUTING	4	8	02/Mar/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403141	POWER SYSTEM OPERATION AND CONTROL	4	7	17/Feb/2022	04.00 PM To 06.00 PM

Course_Name	SUBJECT CODE	SUBJECT NAME	YEAR	SEM.	Exam_Date	TIME SLOT
B.E.(2015 PAT.)(ELECTRICAL)	403142	PLC AND SCADA APPLICATIONS	4	7	18/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403145	CONTROL SYSTEM II	4	7	21/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403143B	POWER QUALITY	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403143C	RENEWABLE ENERGY SYSTEMS	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403144A	RESTRUCTURING AND DEREGULATION	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403144B	ELECTROMAGNETIC FIELDS	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403144C	EHVAC TRANSMISSION	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403144D	ELECTRIC AND HYBRID VEHICLES	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	410301	Machine Learning (MINOR)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	410401	Internet of Things and Embeded Security (MINOR)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	410501	Machine Learning and Data Science (MINOR)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	410701	Virtual Reality in Game Development(MINOR)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	411601	Machine Learning for Internet of Things(MINOR)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403147	SWITCHGEAR AND PROTECTION	4	8	25/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403148	POW.ELECTR. CONTROLLED DRIVES	4	8	26/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403149A	HIGH VOLTAGE ENGINEERING	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403149B	HVDC AND FACTS	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403150A	SMART GRID	4	8	02/Mar/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRICAL)	403150C	ILLUMINATION ENGINEERING	4	8	02/Mar/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRONICS)	404201	VLSI DESIGN	4	7	17/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRONICS)	404202	ADVANCED POWER ELECTRONICS	4	7	18/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRONICS)	404203	ELECTRONICS SYSTEM DESIGN	4	7	21/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRONICS)	404204C	EMBEDDED SYSTEMS & RTOS	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRONICS)	404204D	INTERNET OF THINGS	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRONICS)	404205A	MOBILE COMMUNICATION	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRONICS)	404205B	BIO-MEDICAL ELECTRONICS	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRONICS)	410301	Machine Learning (HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRONICS)	410401	Internet of Things and Embeded Security(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM

Course_Name	SUBJECT CODE	SUBJECT NAME	YEAR	SEM.	Exam_Date	TIME SLOT
B.E.(2015 PAT.)(ELECTRONICS)	410501	Machine Learning and Data Science(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRONICS)	410701	Virtual Reality in Game Development(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(ELECTRONICS)	411601	Machine Learning for Internet of Things(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414453	INFORMATION AND CYBER SECURITY	4	7	17/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414454	MACHINE LEARNING AND APPLICATIONS	4	7	18/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414455	SOFTWARE DESIGN AND MODELING	4	7	21/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414456A	WIRELESS COMMUNICATIONS	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414456B	NATURAL LANGUAGE PROCESSING	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414456C	USABILITY ENGINEERING	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414456D	MULTICORE AND CONCURRENT SYSTEMS	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414456E	BUSINESS ANALYTICS AND INTELLIGENCE	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414457A	SOFTWARE DEFINED NETWORKS	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414457B	SOFT COMPUTING	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414457C	SOFTWARE TESTING AND QUALITY ASSURANCE	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414457E	GAMIFICATION	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	410301	Machine Learning (HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	410401	Internet of Things and Embeded Security(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	410501	Machine Learning and Data Science(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	410701	Virtual Reality in Game Development(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	411601	Machine Learning for Internet of Things(HONOURS)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414462	Distributed Computing System	4	8	25/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414463	UBIQUITOUS COMPUTING	4	8	26/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414464A	INTERNET OF THINGS (IOT)	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414464B	INFO. STORAGE & RETRIEVAL	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414464D	INTERNET AND WEB PROGRAMMING	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(INFORMATION TECHNOLOGY)	414465D	Social Media Analytics	4	8	02/Mar/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	302049	REFRIG. & AIR CONDITIONING	4	7	17/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402041	HYDRAULICS AND PNEUMATICS	4	7	17/Feb/2022	04.00 PM To 06.00 PM

Course_Name	SUBJECT CODE	SUBJECT NAME	YEAR	SEM.	Exam_Date	TIME SLOT
B.E.(2015 PAT.)(MECHANICAL)	402042	CAD CAM AUTOMATION	4	7	18/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402043	DYNAMICS OF MACHINERY	4	7	21/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402044A	FINITE ELEMENT ANALYSIS	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402044B	COMPUTATIONAL FLUID DYNAMICS	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402044C	HEATING VENTILATION AND AIR CONDITIONING	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402045A	AUTOMOBILE ENGINEERING	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402045B	OPERATION RESEARCH	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402045C	ENERGY AUDIT AND MANAGEMENT	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	410301	Machine Learning (MINOR)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	410401	Internet of Things and Embeded Security (MINOR)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	410501	Machine Learning and Data Science (MINOR)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	410701	Virtual Reality in Game Development(MINOR)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	411601	Machine Learning for Internet of Things(MINOR)	4	7	24/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402047	ENERGY ENGINEERING	4	8	25/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402048	MECHANICAL SYSTEM DESIGN	4	8	26/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402049A	Tribology	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402049B	INDUSTRIAL ENGINEERING	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402049C	ROBOTICS	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402050A	Advanced Manufacturing Processes	4	8	02/Mar/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402050B	SOLAR & WIND ENERGY	4	8	02/Mar/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL)	402050C	PRODUCT DESIGN & DEVELOPMENT	4	8	02/Mar/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL-SANDWICH)	402064	AUTOMOBILE ENGINEERING (SELF-STUDY - III)	4	7	22/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL-SANDWICH)	402065	PLANT ENGINEERING AND MAINTENANCE (SELF-STUDY - IV)	4	7	23/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL-SANDWICH)	402048	MECHANICAL SYSTEM DESIGN*	4	8	26/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL-SANDWICH)	402066	MECHANICAL VIBRATIONS	4	8	28/Feb/2022	04.00 PM To 06.00 PM
B.E.(2015 PAT.)(MECHANICAL-SANDWICH)	402069A	ENERGY AUDIT AND MANAGEMENT	4	8	02/Mar/2022	04.00 PM To 06.00 PM

Note: The Backlog Students of B.E. 2012 Pattern will have to appear for equivalance subject of B.E. 2015 Pattern.

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING
A.Y. 2020-21, Sem- I

Date:- 23/06/2021

SAVITRIBAI PHULE PUNE UNIVERSITY
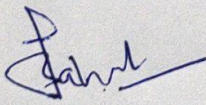### IN SEM EXAMINATION JUNE 2021 TIME TABLE

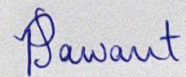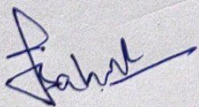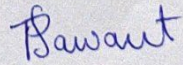## SPPU In sem exam(Online) Time Table carry forward students
S.E. (E&TC)

Duration : 26/06/2021 to 30/06/2021

S.N.	SUBJECT	DAY & DATE	TIME	Contact Subject Incharge
In sem Examination				
1	Electronic Circuits	Saturday, 26/06/2021	10:00 am – 11:00 am	Ms. R. R. Malekar
2	Electrical Circuits	Sunday, 27/06/2021	10:00 am – 11:00 am	Mr.J. M. Bakliwal
3	Data Structures	Monday , 28/06/2021	10:00 am – 11:00 am	Ms. M. R. Rathod
4	Digital Circuits	Tuesday, 29/06/2021	10:00 am – 11:00 am	Ms.H. N. Burande
5	Engineering Mathematics - III	Wednesday, 30/06/2021	10:00 am – 11:00 am	Mr. S. L. Balagaon

Important Note:

✔ SPPU In sem exam will be conducted on Moodle Platform or google form,Students are informed to call Individual subject Incharge for any query.

✔ All students have to use their Moodle Login ID & password to appear for the examination.

✔ Question papers are MCQs based.

✔ Duration of examination is 40 minutes

Exam Coordinator

HOD

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING
A.Y. 2020-21, Sem-1

Date:- 23/06/2021

SAVITRIBAI PHULE PUNE UNIVERSITY
IN SEM EXAMINATION JUNE 2021 TIME TABLE
SPPU In sem exam(Online) Time Table carry forward students
T.E. (E&TC)

Duration : 26/06/2021 to 30/06/2021

S.N.	SUBJECT	DAY & DATE	TIME	Contact Subject Incharge
In sem Examination				
1	Digital Communication	Saturday, 26/06/2021	12:00 noon – 01:00 pm	Dr. A. M. Solanke
2	Digital Signal Processing	Sunday, 27/06/2021	12:00 noon – 01:00 pm	Ms. S. S. Tavse
3	Microcontrollers	Monday , 28/06/2021	12:00 noon – 01:00 pm	Mr. A. G. Dakre
4	Electromagnetics	Tuesday, 29/06/2021	12:00 noon – 01:00 pm	Dr. A. A. Kunte
5	Mechatronics	Wednesday, 30/06/2021	12:00 noon – 01:00 pm	Mr. V. B. Deokamble

Important Note:

✔ SPPU In sem exam will be conducted on Moodle Platform or google form,Students are informed to call Individual subject Incharge for any query.

✔ All students have to use their Moodle Login ID & password to appear for the examination.

✔ Question papers are MCQs based.

✔ Duration of examination is 40 minutes

Exam Coordinator

HOD

Marathwada Mitra Mandal's
COLLEGE OF ENGINEERING, PUNE-52
Accredited with 'A' Grade by NAAC / Recipients of "Best College Award 2019" by SPPU

DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING
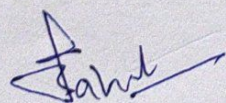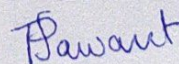**A.Y. 2020-21, Sem-1**

Date:- 23/06/2021

SAVITRIBAI PHULE PUNE UNIVERSITY
IN SEM EXAMINATION MARCH 2021 TIME TABLE
SPPU In sem exam(Online) Time Table carry forward students
B.E. (E&TC)

Duration : 26/06/2021 to 30/06/2021

S. N.	SUBJECT	DAY & DATE	TIME	Contact Subject Incharge
		In sem Examination		
1	VLSI Design & Technology	Saturday, 26/06/2021	2:00 pm – 03:00 pm	Ms. H. N. Burande
2	Computer Networks & Security	Sunday, 27/06/2021	2:00 pm – 03:00 pm	Ms. M. A. Dudhedia
3	Radiation & Microwave Techniques	Monday , 28/06/2021	2:00 pm – 03:00 pm	Dr. A. M. Solanke
4	Internet of Things	Tuesday, 29/06/2021	2:00 pm – 03:00 pm	Ms. P. S. Sawant
5	Electronics Product Design	Wednesday, 30/06/2021	2:00 pm – 03:00 pm	Ms. S. N. Deshmukh

Important Note:

✔ SPPU In sem exam will be conducted on Moodle Platform or google form, Students are informed to call Individual subject Incharge for any query.

✔ All students have to use their Moodle Login ID & password to appear for the examination.

✔ Question papers are MCQs based.

✔ Duration of examination is 40 minutes

Exam Coordinator   HOD

Internal Assessment
Unit Test Question Papers, Solution, Sample Sheets, Marks

Machine Learning Unit Test II

Wednesday   24/02/2021   Time: 2.00pm to 3.00pm   Class: BE-Comp II
MCQs : 10mks   Theory: 20 mks   Total: 30mks

Name of Student *

Varun Gole

Roll no *

BE211

1. What is the purpose of performing cross-validation *   1 point

○ To assess the predictive performance of the models

○ To judge how the trained model performs outside the sample on test data

◉ Both A and B

○ None of above

2. _____ allows you to rebuild a sample starting from a sparse dictionary of atoms (similar to principal components, but without constraints about the independence).   * 1 point

○ Dictionary learning

⦿ Machine Learning

○ Both A and B

○ None of the above

3. Scikit-learn is considered as the best library for *   1 point

○ Model building

○ Evaluation metrics

○ Hyper-parameter tuning

⦿ All of the above

4. We use Dimensionality Reduction techniques   * 1 point
        a. For there are too many variables/predictors which increase the chances of multi-collinearity   b. Which increase the chances of over-fitting while model building and these variables become redundant.

○ Only a

○ Only b

⦿ Both (a) and (b)

○ None of above

5. One hot encoding *   1 point

⦿ Increase the dimensionality of a data set

◯ Decrease the dimensionality of a data set

◯ Depends upon the use case

◯ All of the above

6. Which of the following techniques would perform better for reducing dimensions   * 1 point
of a data set?

⦿ Removing columns which have too many missing values

◯ Removing columns which have high variance in data

◯ Removing columns with dissimilar data trends

◯ None of these

7. The most popularly used dimensionality reduction algorithm is Principal   * 1 point
Component Analysis (PCA). Which of the following is/are true about PCA?   1.
PCA is an unsupervised method   2. It searches for the directions that
data have the largest variance   3. Maximum number of principal
components <= number of features   4. All principal components are
orthogonal to each other

◯ 1 and 2

◯ 1 and 3

⦿ 2 and 3

◯ All of the above

8.Under which condition SVD and PCA produce the same projection result? *   1 point

○ When data has zero median

◉ When data has zero mean

○ Both are always same

○ None of these

9. Missing values has become a regular part of the modern-day data collection   * 1 point
procedure. The main reasons for missing values include-   1. Error in
collection   2. Error in recording   3. Non Responsiveness

○ 1 and 2

◉ 1 and 3

○ 1,2 and 3

○ None of above

10. The major reasons for performing data preprocessing includes-   1. Lack of * 1 point
appropriate variables or contains missing values   2. Inconsistency or
Discrepancy in the recorded data   3. Remove outliers or errors

○ 1 and 2

◉ 1 and 3

○ 2 and 3

○ 1,2 and 3

11. Explain Principal Component Analysis (PCA) and its use. *   4 points

⤓ 1 - varun gole.jpeg

12. Explain the criteria or methodology for creation of Training and Testing data   * 4 points
sets in machine learning methods

⤓ 2 - varun gole.jpeg

13. State and explain different mechanisms for managing missing features in a   * 4 points
dataset.

⤓ 3 - varun gole.jpeg

14. Describe categorical data and its significance in classification problems. *   4 points

⤓ 4 - varun gole.jpeg

15. Explain dictionary learning. State its applications *   4 points

⤓ 5 - varun gole.jpeg

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Google Forms

Machine Learning Unit Test II

Wednesday   24/02/2021   Time: 2.00pm to 3.00pm   Class: BE-Comp II
                             MCQs : 10mks   Theory: 20 mks

Total: 30mks

* Required

1.   Name of Student *

   _____

2.   Roll no *

   _____

3.   1. What is the purpose of performing cross-validation *   1 point

   Mark only one oval.

   ( ) To assess the predictive performance of the models

   ( ) To judge how the trained model performs outside the sample on test data

   ( ) Both A and B

   ( ) None of above

4.   2. _____ allows you to rebuild a sample starting from a sparse   * 1 point
   dictionary of atoms (similar to principal components, but without
   constraints about the independence).

   Mark only one oval.

   ( ) Dictionary learning

   ( ) Machine Learning

   ( ) Both A and B

   ( ) None of the above

5.   3. Scikit-learn is considered as the best library for *   1 point

Mark only one oval.

⚪ Model building

⚪ Evaluation metrics

⚪ Hyper-parameter tuning

⚪ All of the above

6.   4. We use Dimensionality Reduction techniques   * 1 point
                        a. For there are too many variables/predictors
which increase the chances of multi-collinearity   b.
Which increase the chances of over-fitting while model building and
these variables become redundant.

Mark only one oval.

⚪ Only a

⚪ Only b

⚪ Both (a) and (b)

⚪ None of above

7.   5. One hot encoding *   1 point

Mark only one oval.

⚪ Increase the dimensionality of a data set

⚪ Decrease the dimensionality of a data set

⚪ Depends upon the use case

⚪ All of the above

8. 6. Which of the following techniques would perform better for reducing   * 1 point
    dimensions of a data set?

    Mark only one oval.

    ⬭ Removing columns which have too many missing values

    ⬭ Removing columns which have high variance in data

    ⬭ Removing columns with dissimilar data trends

    ⬭ None of these

9. 7. The most popularly used dimensionality reduction algorithm is   * 1 point
    Principal Component Analysis (PCA). Which of the following is/are true
    about PCA?   1. PCA is an unsupervised method   2. It
    searches for the directions that data have the largest variance
     3. Maximum number of principal components <= number of features
         4. All principal components are orthogonal to each other

    Mark only one oval.

    ⬭ 1 and 2

    ⬭ 1 and 3

    ⬭ 2 and 3

    ⬭ All of the above

10. 8.Under which condition SVD and PCA produce the same projection   * 1 point
    result?

    Mark only one oval.

    ⬭ When data has zero median

    ⬭ When data has zero mean

    ⬭ Both are always same

    ⬭ None of these

11.   9. Missing values has become a regular part of the modern-day data   * 1 point
      collection procedure. The main reasons for missing values include-
                1. Error in collection   2. Error in recording   3.
      Non Responsiveness

      Mark only one oval.

      ( ) 1 and 2

      ( ) 1 and 3

      ( ) 1,2 and 3

      ( ) None of above

12.   10. The major reasons for performing data preprocessing includes-   * 1 point
         1. Lack of appropriate variables or contains missing values   2.
      Inconsistency or Discrepancy in the recorded data   3. Remove
      outliers or errors

      Mark only one oval.

      ( ) 1 and 2

      ( ) 1 and 3

      ( ) 2 and 3

      ( ) 1,2 and 3

13.   11. Explain Principal Component Analysis (PCA) and its use. *   4 points

      Files submitted:

14.   12. Explain the criteria or methodology for creation of Training and   * 4 points
      Testing data sets in machine learning methods

      Files submitted:

15.   13. State and explain different mechanisms for managing missing   * 4 points
      features in a dataset.

      Files submitted:

16.   14. Describe categorical data and its significance in classification   * 4 points
      problems.

      Files submitted:

17.   15. Explain dictionary learning. State its applications *   4 points

      Files submitted:

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TEIT- OS Unit Test III

Dear Students, solve the following MCQs as per instructions.
1) Enter correct email id because you will get copy of you responses on same email id.
2) All questions are compulsory.
3) Once the test is submitted you will not be allowed to modify the answer.
4) There are total 15 questions
 1 mark - 10 questions
2 marks - 5 questions

Academic Year 2020-21

* Required

1.   Name of the Student *

_____

2.   Enter Your Roll Number *

_____

1 Mark - 10 Questions

3.   1.A situation where several processes access and manipulate the same   * 1 point
      data concurrently and the outcome of the execution depends on the
      particular order in which access takes place is called _____

      Mark only one oval.

      ◯ starvation

      ◯ data consistency

      ◯ race condition

      ◯ aging

4.   2. Which of the following conditions must be satisfied to solve the critical   * 1 point
      section problem?

      Mark only one oval.

      ◯ All of the mentioned

      ◯ Mutual Exclusion

      ◯ Progress

      ◯ Bounded Waiting

5.   3. Mutual exclusion implies that _____ *   1 point

      Mark only one oval.

      ◯ when another process is in its critical section

      ◯ before a process has made a request to enter its critical section

      ◯ after a process has made a request to enter its critical section and before the
      request is granted

      ◯ none of the mentioned

6.   4. A minimum of _____ variable(s) is/are required to be shared between   * 1 point
      processes to solve the critical section problem.

      Mark only one oval.

      ◯ one

      ◯ two

      ◯ three

      ◯ four

7.   5. Semaphore is a/an _____ to solve the critical section problem. *   1 point

Mark only one oval.

◯ hardware for a system

◯ special program for a system

◯ data

◯ integer variable

8.   6. What are the two atomic operations permissible on semaphores? *   1 point

Mark only one oval.

◯ quit

◯ hold

◯ stop

◯ wait

9.   7. If the semaphore value is negative _____ *   1 point

Mark only one oval.

◯ it is invalid

◯ its magnitude is the number of processes waiting on that semaphore

◯ no operation can be further performed on it until the signal operation is performed on it

◯ none of the mentioned

10.   8. What will happen if a non-recursive mutex is locked more than once? *   1 point

Mark only one oval.

◯ Starvation

◯ Deadlock

◯ Aging

◯ Signaling

11.   9. What are the two kinds of semaphores? *   1 point

Mark only one oval.

⬭ mutex & counting

⬭ counting & decimal

⬭ binary & counting

⬭ decimal & binary

12.   10. Which of the following condition is required for a deadlock to be   * 1 point
       possible?

Mark only one oval.

⬭ all of the mentioned

⬭ mutual exclusion

⬭ a process may hold allocated resources while awaiting assignment of other
   resources

⬭ no resource can be forcibly removed from a process holding it

2 Marks - 5 Questions

13.   1. Which of the following is/are the disadvantages of machine   * 2 points
       instruction approach to enforce mutual exclusion.i) Busy waiting
       employees ii) hard to verify iii) starvation is possible iv) Deadlock is
       possible

Mark only one oval.

⬭ i, iii and iv only

⬭ ii, iii and iv only

⬭ i, ii and iii only

⬭ All i, ii, iii and iv

14. 2. State whether the statements are True or False for the operations of * 2 points
     semaphore.i) A semaphore may be initialized to a non-negative value.ii)
     The wait operation decrements the semaphore valueiii) The single
     operation increments the semaphore value.

     Mark only one oval.

     ◯ i and ii

     ◯ ii and iii

     ◯ i and iv

     ◯ All i, ii and iii

15. 3. The bounded buffer problem is also known as _____ *   2 points

     Mark only one oval.

     ◯ Producer – Consumer problem

     ◯ Dining – Philosophers problem

     ◯ Readers – Writers problem

     ◯ None

16. 4. The dining – philosophers problem will occur in case of   * 2 points
     _____

     Mark only one oval.

     ◯ 6 philosophers and 5 chopsticks

     ◯ 4 philosophers and 5 chopsticks

     ◯ 5 philosophers and 5 chopsticks

     ◯ 3 philosophers and 5 chopsticks

17. 5. A deadlock free solution to the dining philosophers problem
_____

Mark only one oval.

⊙ necessarily eliminates the possibility of starvation

⊙ eliminates any possibility of any kind of problem further

⊙ necessarily eliminates the possibility of deadlock

⊙ does not necessarily eliminate the possibility of starvation

* 2 points

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TEIT- OS Unit Test III

Dear Students, solve the following MCQs as per instructions.
1) Enter correct email id because you will get copy of you responses on same email id.
2) All questions are compulsory.
3) Once the test is submitted you will not be allowed to modify the answer.
4) There are total 15 questions
 1 mark - 10 questions
2 marks - 5 questions

Academic Year 2020-21

Email *

akashchopade2018.it@mmcoe.edu.in

Name of the Student *

Akash

Enter Your Roll Number *

12

1 Mark - 10 Questions

1.A situation where several processes access and manipulate the same data   * 1 point
concurrently and the outcome of the execution depends on the particular order in
which access takes place is called _____

○ starvation

○ data consistency

◉ race condition

○ aging

2. Which of the following conditions must be satisfied to solve the critical section   * 1 point
problem?

◉ All of the mentioned

○ Mutual Exclusion

○ Progress

○ Bounded Waiting

3. Mutual exclusion implies that _____ *   1 point

◉ when another process is in its critical section

○ before a process has made a request to enter its critical section

○ after a process has made a request to enter its critical section and before the request is granted

○ none of the mentioned

4. A minimum of _____ variable(s) is/are required to be shared between processes to   * 1 point
solve the critical section problem.

○ one

◉ two

○ three

○ four

5. Semaphore is a/an _____ to solve the critical section problem. *   1 point

○ hardware for a system

○ special program for a system

○ data

◉ integer variable

6. What are the two atomic operations permissible on semaphores? *   1 point

○ quit

○ hold

○ stop

◉ wait

7. If the semaphore value is negative _____ *   1 point

○ it is invalid

◉ its magnitude is the number of processes waiting on that semaphore

○ no operation can be further performed on it until the signal operation is performed on it

○ none of the mentioned

8. What will happen if a non-recursive mutex is locked more than once? *   1 point

○ Starvation

◉ Deadlock

○ Aging

○ Signaling

9. What are the two kinds of semaphores? *   1 point

○ mutex & counting

○ counting & decimal

◉ binary & counting

○ decimal & binary

10. Which of the following condition is required for a deadlock to be possible? *   1 point

◉ all of the mentioned

◯ mutual exclusion

◯ a process may hold allocated resources while awaiting assignment of other resources

◯ no resource can be forcibly removed from a process holding it

2 Marks - 5 Questions

1. Which of the following is/are the disadvantages of machine instruction approach to * 2 points
enforce mutual exclusion.i) Busy waiting employees ii) hard to verify iii) starvation is
possible iv) Deadlock is possible

◯ i, iii and iv only

◯ ii, iii and iv only

◯ i, ii and iii only

◉ All i, ii, iii and iv

2. State whether the statements are True or False for the operations of semaphore.i) * 2 points
A semaphore may be initialized to a non-negative value.ii) The wait operation
decrements the semaphore valueiii) The single operation increments the semaphore
value.

◯ i and ii

◯ ii and iii

◯ i and iv

◉ All i, ii and iii

3. The bounded buffer problem is also known as _____ *   2 points

○ Producer – Consumer problem

○ Dining – Philosophers problem

○ Readers – Writers problem

○ None

4. The dining – philosophers problem will occur in case of _____ *   2 points

○ 6 philosophers and 5 chopsticks

○ 4 philosophers and 5 chopsticks

● 5 philosophers and 5 chopsticks

○ 3 philosophers and 5 chopsticks

5. A deadlock free solution to the dining philosophers problem _____ *   2 points

○ necessarily eliminates the possibility of starvation

○ eliminates any possibility of any kind of problem further

○ necessarily eliminates the possibility of deadlock

● does not necessarily eliminate the possibility of starvation

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Google Forms

Timestamp	Email address	Score	Name of the Student	Enter Your Roll Number
9-18-2020 14:12:26	preetibadave2018.it@mmcoe.edu.in	20 / 20	Preeti Badave	4
8-3-2020 12:04:48	gauravbelekar2018.it@mmcoe.edu.in	17 / 20	Gaurav Belekar	6
9-30-2020 15:16:52	sachinchavan2018.it@mmcoe.edu.in	20 / 20	sachin chavan	10
8-3-2020 11:52:33	chetanurkudkar2017.it@mmcoe.edu.in	18 / 20	Chetan Urkudkar	11
8-3-2020 11:44:38	akashchopade2018.it@mmcoe.edu.in	17 / 20	Akash	12
9-30-2020 15:17:59	darshanrajput2018.it@mmcoe.edu.in	19 / 20	Darshan Rajput	13
9-30-2020 15:17:01	pushkardashpute2018.it@mmcoe.edu.in	20 / 20	Pushkar dashpute	14
8-3-2020 11:50:59	prathameshdeshmukh2018.it@mmcoe.edu.i	13 / 20	Prathamesh Deshmukh	15
8-4-2020 10:33:07	harshgangawane2018.it@mmcoe.edu.in	20 / 20	Harsh Gangawane	16
8-3-2020 11:51:14	nikhilagazula2018.it@mmcoe.edu.in	20 / 20	Nikhila Gazula	17
10-2-2020 18:26:21	abhaygore2018.it@mmcoe.edu.in	20 / 20	Abhay Gore	19
8-31-2020 19:55:35	amitgujar2018.it@mmcoe.edu.in	17 / 20	Amit Gujar	20
8-26-2020 10:46:54	bhagyeshhatwalne2018.it@mmcoe.edu.in	16 / 20	Bhagyesh Hatwalne	21
8-3-2020 11:52:15	vaibhavikalinkar2018.it@mmcoe.edu.in	20 / 20	vaibhavi kalinkar	23
8-3-2020 12:53:26	tanvikarale2018.it@mmcoe.edu.in	18 / 20	Tanvi Karale	25
8-3-2020 12:46:32	shubhamkatkar2018.it@mmcoe.edu.in	20 / 20	Shubham Katkar	26
8-3-2020 11:44:43	jayshreekeskar2018.it@mmcoe.edu.in	17 / 20	Jayshree Keskar	27
8-3-2020 11:51:45	omkarkeskar2018.it@mmcoe.edu.in	19 / 20	OMKAR C. KESKAR	28
9-30-2020 15:16:57	rutujakhire2018.it@mmcoe.edu.in	19 / 20	Rutuja Khire	29
8-3-2020 11:52:15	anjalikhivansara2019.it@mmcoe.edu.in	20 / 20	Anjali Khivansara	30
8-3-2020 11:56:28	ashmitkhobragade2018.it@mmcoe.edu.in	19 / 20	Ashmit Khobragade	31
8-3-2020 11:48:17	piyushkolhe2018.it@mmcoe.edu.in	18 / 20	Piyush Milind Kolhe	32
9-30-2020 15:16:02	abhisheklalwani2018.it@mmcoe.edu.in	20 / 20	Abhishek Lalwani	33
8-3-2020 11:53:30	minakshilanke2018.it@mmcoe.edu.in	17 / 20	MINAKSHI R. LANKE	34
8-3-2020 12:36:01	lolepadmaja@gmail.com	20 / 20	PADMAJA LOLE	35
8-3-2020 11:51:18	madhuramagdum2019.it@mmcoe.edu.in	18 / 20	Madhura Magdum	36
8-3-2020 11:50:53	nikitashintre2019.it@mmcoe.edu.in	18 / 20	NIKITA SHINTRE	38
8-3-2020 11:53:31	gaurangpalaskar2018.it@mmcoe.edu.in	19 / 20	Gaurang Palaskar	39
9-19-2020 12:13:08	nageshpawde2018.it@mmcoe.edu.in	20 / 20	NAGESH PAWDE	40
8-3-2020 11:55:54	aishwaryaporwal2018.it@mmcoe.edu.in	18 / 20	Aishwarya Porwal	41
8-3-2020 12:05:34	prajaktaraut2018.it@mmcoe.edu.in	20 / 20	Prajakta Raut	43
8-31-2020 14:40:05	bhalerutuja2016.it@mmcoe.edu.in	20 / 20	Rutuja bhale	44
8-3-2020 11:58:04	sairajkhandagale2018.it@mmcoe.edu.in	20 / 20	Sairaj Dilip Khandagale	45
8-3-2020 11:52:58	vedantsalvi2018.it@mmcoe.edu.in	17 / 20	Vedant Salvi	46
8-3-2020 11:58:45	rohitsanap2018.it@mmcoe.edu.in	20 / 20	Rohit Sanap	47
8-3-2020 11:48:09	sejalsarolkar2018.it@mmcoe.edu.in	18 / 20	sejal sarolkar	49

8-3-2020 11:55:21	sauradhmahalankar019.it@mmcoe.edu.in	17 / 20	Sauradh mahalankar	50
8-3-2020 11:51:06	muskanshaikh2019.it@mmcoe.edu.in	18 / 20	Muskan Shaikh	51
8-3-2020 11:56:22	shambirajdar2018.it@mmcoe.edu.in	17 / 20	Sham Birajdar	52
8-3-2020 11:45:39	shrutishete2018.it@mmcoe.edu.in	15 / 20	Shruti Shete	53
8-3-2020 11:48:07	Snehakambley2017.lt@mmcoe.edu.in	15 / 20	Sneha kambley	57
8-3-2020 11:51:18	seemasomwanshi2019.it@mmcoe.edu.in	18 / 20	Seema Somwanshi	58
8-3-2020 11:52:04	sonalisuryavanshi2018.it@mmcoe.edu	16 / 20	Sonali Suryavanshi	59
9-30-2020 15:16:54	samruddhivelapurkar2018.it@mmcoe.edu.in	20 / 20	samruddhi velapurkar	61
8-3-2020 11:51:37	amitvirkar2018.it@mmcoe.edu.in	20 / 20	Amit Virkar	62
8-3-2020 12:12:53	vishalingle2019.it@mmcoe.edu.in	19 / 20	Vishal Ingle	63
8-3-2020 11:51:47	sadhanawaghmode2019.it@mmcoe.edu.in	18 / 20	Waghmode sadhana	64
9-30-2020 15:17:06	yashdixit2018.it@mmcoe.edu.in	20 / 20	Yash Dixit	65
10-3-2020 11:21:44	varunmalpani2017.it@mmcoe.edu.in	17 / 20	Varun Malpani	66
8-3-2020 12:03:20	vinayaktumbagi2019.it@mmcoe.edu.in	17 / 20	Vinayak Tumbagi	67
8-4-2020 12:41:53	panchalkashinath2017.it@mmcoe.edu.in	13 / 20	Santoshi Panchal	67
8-3-2020 11:46:26	poojasonawane2017.it@mmcoe.edu.in	15 / 20	Pooja Sonawane	68
8-3-2020 11:48:38	dhoreabolee2016.it@mmcoe.edu.in	16 / 20	ABOLEE DHORE	69
8-3-2020 11:48:09	himanichaudhary.it@mmcoe.edu.in	17 / 20	Himani Chaudhari	76
8-3-2020 12:03:39	akshaytale2018.it@mmcoe.edu.in	18 / 20	Akshay Shahaji Tale	02
8-3-2020 11:51:23	amitkadam2018.it@mmcoe.edu.in	16 / 20	Kadam Amit Vijaykuma	03
8-3-2020 11:52:22	swastibali2018.it@mmcoe.edu.in	20 / 20	Swasti Bali	05
8-31-2020 14:36:04	shrutibhagwat2018.it@mmcoe.edu.in	20 / 20	Shruti Bhagwat	07
8-3-2020 12:50:18	bhargavibindu2018.it@mmcoe.edu.in	19 / 20	Bhargavi Bindu	09
10-20-2020 19:11:37	ishaghaisas2018.it@mmcoe.edu.in	15 / 20	Isha Ghaisas	18
5-4-2021 12:17:40	swaroopsatav2017.it@mmcoe.edu.in	19 / 20	Swaroop Dilip Satav	71

Marathwada Mitramandal's

COLLEGE OF ENGINEERING

S.No.18, Plot No.5/3, Karvenagar, Pune-411 052

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Recipient of 'Best College Award 2019' of SPPU
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Internal Assessment
Assignments

DEPARTMENT OF COMPUTER ENGINEERING

Subject: SMD

Class: TE Computer
Assignment on Unit 4: Architecture Design

CO4- Apply different architectural designs to transfer applications into proper model

Note: All the students are informed to attempt the assignment questions as stated below. Refer to your **Batch name** *(as per pratical batches) and the* **Set name**.

Set	BTL	Batch A	Batch B	Batch C
Set A	BTL 1	1,2	2,3	1,3
	BTL 2	4,5	5,6	4,6
	BTL 3	7,8	8,9	7,9
Set B	BTL 3	7,8	8,9	7,9
	BTL 4	10, 11	11, 12	10, 12
	BTL 5	13	14	13
Set C	BTL 4	10, 11	11, 12	10, 12
	BTL 5	13	14	13
	BTL 6	15	15	15

Set A-
TC210, TC212,TC217,TC218,TC223,TC230,TC233,TC239,TC243,TC246,TC249,TC251,TC261,TC266
TC267,TC273,TC274,TC275,TC276,TC277,TC278,TC279,TC280,TC281
Set B-
TC201,TC202,TC203,TC206,TC208,TC209,TC215,TC220,TC221,TC227,TC228,TC231,TC234
TC235,TC238,TC244,TC245,TC250,TC252,TC253,TC254,TC256,TC258,TC259,TC263,TC270
TC272
Set C-
TC204,TC205,TC207,TC211,TC213,TC214,TC216,TC219,TC222,TC224,TC225,TC226,TC229,
TC232,TC236,TC237,TC240,TC241,TC242,TC247,TC248,TC255,TC257,TC260,TC262,TC264
TC265,TC268,TC269,TC271

Sr. No.	Questions
	Blooms Level 01 'Recall'
Q. 1	**Choose** Why does architectural design occurs during product design? a) Stakeholders must convinced that their needs will be met, which may be difficult without demonstrating how the engineers plan to build the product

	b) Product designers must judge the feasibility of their designs c) Project planners must have some idea about what software must be built to create schedules and allocate resources d)All of the mentioned (ans: B)
Q. 2	**Define** architectural Design of a Software system
Q. 3	**List** the important characteristics of real time software architecture.
Blooms Level 02 'Understand'	
Q. 4	**Explain** the need of software architecture design.
Q. 5	**Describe** Component based software architecture?
Q. 6	**Explain** mutiple client multiple service architectural pattern with a suitable example.
Blooms Level 03 'Apply'	
Q. 7	**Demonstrate** with an example,Dynamic view of software architecure
Q. 8	**Sketch** synchronous communication pattern in client server architecure
Q. 9	**Illustrate** important design principles for service oriented architecture
Blooms Level 04 'Analyse'	
Q. 10	Compare Structural view and dynamic view architecure of software system
Q. 11	Explain port,Provided interfaces and required interfaces with an example.
Q. 12	Examine the functionality of Broker pattern for service oriented architecure
Blooms Level 05 'Evaluate'	
Q. 13	Justify, how it is possible for a component to provide more than one interface.(ans:pg 303 hassan gomma)
Q. 14	Explain object oriented architecture with data abstraction
Blooms Level 06 'Create'	
Q. 15	Design use case diagram,class diagram and communication diagram for the ATM System

Ms. Pradnya Mehta
Course Coordinator